

First Named Inventor	Frankie F. Roohparvar	INFORMATION DISCLOSURE STATEMENT
Serial No.	Unknown	
Filing Date	Filed herewith	
Group Art Unit	Unknown	
Examiner Name	Unknown	
Confirmation No.	Unknown	
Attorney Docket No.	400.042US02	
Title: WRITE AND ERASE PROTECTION IN A SYNCHRONOUS MEMORY		

Commissioner for Patents
M.S.: Patent Application
P. O. Box 1450
Alexandria, VA 22313-1450

In compliance with 37 C.F.R. §§ 1.56 and 1.97, *et seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified Application. Applicant respectfully requests that this Information Disclosure Statement be entered and the references listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to MPEP §609, Applicant further requests that the Examiner initial next to each reference on the Form 1449 to indicate that the listed references have been considered. Applicant further requests that a copy of the initialed Form 1449 be returned with the next official communication.

Further pursuant to MPEP §609, because all of the references listed on the attached Form 1449 have been previously submitted and made of record in the parent application, U.S. Patent Application 09/648,508, filed August 25, 2000, copies of the references previously made of record in the parent application are not submitted herewith.

If the Examiner has any questions or concerns regarding this application, please contact the undersigned at the number listed below.

Date: 1/21/04

Respectfully submitted,



Kenneth W. Bolvin
Reg. No. 34,125

Attorneys for Applicant
Leffert Jay & Polglaze, P.A.
P.O. Box 581009
Minneapolis, MN 55458-1009

T - 612/312-2200
F - 612/312-2250

First Named Inventor	Frankie F. Roohparvar	INFORMATION DISCLOSURE STATEMENT FORM PTO-1449
Serial No.	Unknown	
Filing Date	Filed Herewith	
Group Art Unit	Unknown	
Examiner Name	Unknown	
Confirmation No.	Unknown	
Attorney Docket No.	400.042US02	

Title: WRITE AND ERASE PROTECTION IN A SYNCHRONOUS MEMORY

Sheet 1 of 1

U.S. Patent References				
Examiner Initials	Document/Patent No.	Issue/Publication Date	Name	Filing Date
	2002/0095545	07/18/02	Dalvi et al.	04/21/00
	4,612,632	09/16/86	Olson	12/10/84
	5,041,886	08/20/91	Lee	09/25/89
	5,526,364	06/11/96	Roohparvar	02/10/95
	5,537,354	07/16/96	Mochizuki et al.	12/14/94
	5,577,195	11/19/96	Gochi	09/03/93
	5,566,323	10/15/96	Ugon	10/24/94
	5,592,641	01/07/97	Fandrich et al.	06/30/93
	5,600,605	02/04/97	Schaefer	06/07/95
	5,666,321	09/09/97	Schaefer	09/01/95
	5,751,039	05/12/98	Kauffman et al.	09/19/97
	5,787,040	07/28/98	Leon et al.	08/21/97
	5,787,457	07/28/98	Miller et al.	10/18/96
	5,812,446	09/22/98	Tailliet	03/28/96
	5,936,903	08/10/99	Jeng et al.	08/07/98
	5,943,263	08/24/99	Roohparvar	01/08/97
	5,995,438	11/30/99	Jeng, et al.	08/07/98
	6,026,465	02/15/00	Mills et al.	06/18/97
	6,028,798	02/22/00	Roohparvar	03/11/97
	6,137,133	10/24/00	Kauffman et al.	05/11/98
	6,141,247	10/31/00	Roohparvar et al.	12/21/98
	6,654,847	11/25/03	Roohparvar et al.	06/30/00

Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	T?
	Country	No.			

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.
	Keeth, et al., "DRAM circuit design: a tutorial," IEEE Press, 2001, pp.16-23, 142-153
	Micron Semiconductor Products, Inc., "2Mb, Smart 5 BIOS-Optimized Boot Block Flash Memory," <u>Flash Memory</u> www.micron.com, copyright 2000, Micron Technology, Inc., pp. 1-12
	Micron, "16 Mb: x16 SDRAM" <u>Synchronous DRAM</u> , www.micron.com, copyright 1999 Micron Technology, Inc., pp. 1-51

Examiner Signature	Date Considered
--------------------	-----------------

*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.